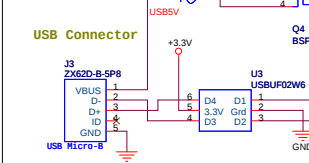
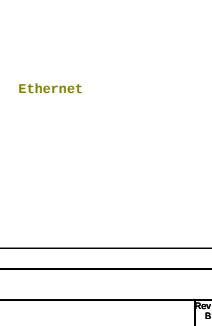
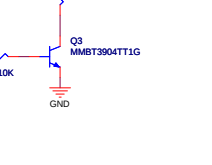
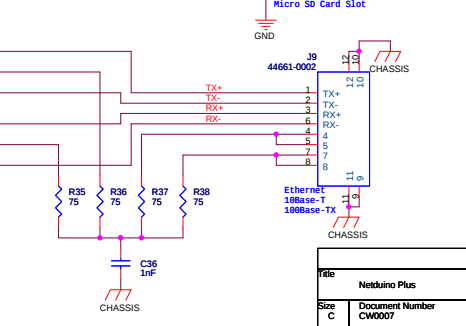
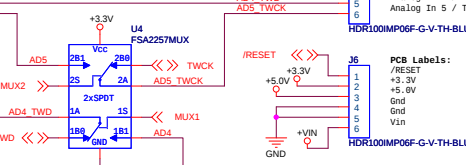
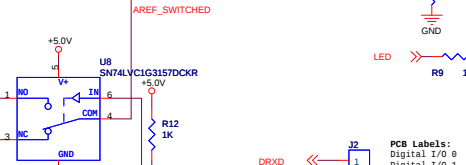
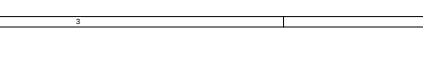
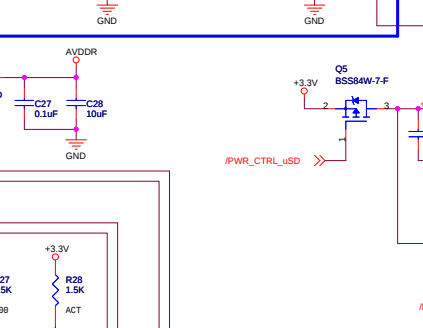
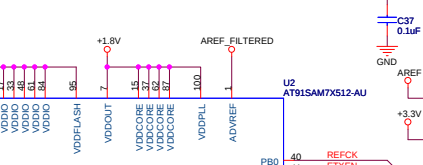
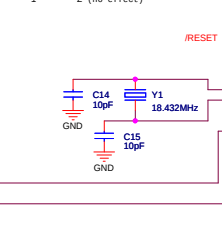


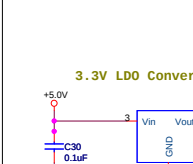
```
If +VIN exists, all rails driven by +VIN
If +VIN not present, all rails driven by USB Power
```

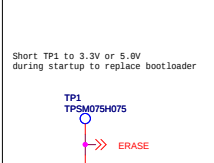
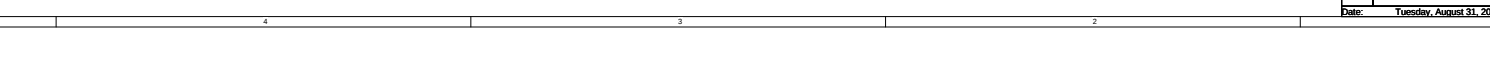
Switch 1 Control of /RESET

Figure 1. The effect of the number of trials on the number of correct responses.





+5.0V

 TD1 |

HDR100IMP08-F-V-TH-BL

PCB Labels:

Pin	Label
1	Digital I/O 8
2	Digital I/O 9
3	Digital I/O 10
4	Digital I/O 11
5	Digital I/O 12
6	Digital I/O 13
7	Gnd
8	Aref

HDR100IMP08-F-V-TH-BL

J6

1
2
3
4
5
6

PCB Labels:
/RESET
+3.3V
+5.0V
Gnd
Gnd
Vin

HDR100IMP6F-G-V-TH-BL

DO NOT PLACE

J8
DM3AT-SF-PEJ

NC
CS
DI
Vdd
CLK
Vss
DO
RSV

CARD IN SW

Wiring diagram for the Base-TX connector. The connector has 11 pins. The wiring is as follows:

- Pin 1: TX-
- Pin 2: RX+
- Pin 3: RX-
- Pin 4: TX+
- Pin 5: TX-
- Pin 6: RX+
- Pin 7: RX-
- Pin 8: TX+
- Pin 9: TX-
- Pin 10: RX+
- Pin 11: RX-

The connector is connected to a CHASSIS ground.