**MAIN IC**

- U1
  - INT_XL
  - DPDY_MAG
  - SCL/SCL
  - CS_XL
  - CS_MAG
  - CS_MAG
  - SDI/SDO
  - INT_MAG
  - C1

**JUMPERS & PULL-UPS**

- SJ1
- SJ2
- R2
- R3
- R4
- R5
- R6

I2C:
- XL slave address: 0x1D
- MAG slave address: 0x1E

Jumpers are connected by default and used by both I2C and SPI. They may be cut to tune an I2C bus when multiple devices are being used.

**MULTI RAIL POWER SEQUENCER**

- UCC = 2.65V to 4.3V

Each output adds a 2ms delay (AE). Using flag 1 to delay VDD 2ms from VDD_10.

**HEADERS**

- JP1
- JP2

**BYPASS**

- C1
- C2
- C3