### Galileo I/O Mappings

<table>
<thead>
<tr>
<th>Arduino IDE ID</th>
<th>Source</th>
<th>Cypr GPIO Pin</th>
<th>Linux GPIO ID</th>
<th>Dir</th>
<th>Muxed with</th>
<th>Initial Setup</th>
</tr>
</thead>
<tbody>
<tr>
<td>IO0</td>
<td>Cypr</td>
<td>GPORT4_BIT6_PWM2</td>
<td>50</td>
<td>N/A</td>
<td>BI</td>
<td>UART0_RXD I w/ pullup off</td>
</tr>
<tr>
<td>IO1</td>
<td>Cypr</td>
<td>GPORT4_BIT7_PWM0</td>
<td>51</td>
<td>N/A</td>
<td>BI</td>
<td>UART0_TXD I w/ pullup off</td>
</tr>
<tr>
<td>IO2</td>
<td>SoC (Cypr)</td>
<td>GPIO&lt;6&gt;</td>
<td>(GPORT0_BIT4_PWM7)</td>
<td>14 (32*)</td>
<td>-</td>
<td>0</td>
</tr>
<tr>
<td>IO3</td>
<td>SoC (Cypr)</td>
<td>GPIO&lt;7&gt;</td>
<td>(GPORT0_BIT2_PWM3)</td>
<td>15</td>
<td>3</td>
<td>1</td>
</tr>
<tr>
<td>IO4</td>
<td>Cypr</td>
<td>GPORT1_BIT4_PWM6</td>
<td>28</td>
<td>-</td>
<td>BI</td>
<td>-</td>
</tr>
<tr>
<td>IO5</td>
<td>Cypr</td>
<td>GPORT0_BIT1_PWM5</td>
<td>17</td>
<td>5</td>
<td>-</td>
<td>BI</td>
</tr>
<tr>
<td>IO6</td>
<td>Cypr</td>
<td>GPORT0_BIT0_PWM6</td>
<td>24</td>
<td>6</td>
<td>-</td>
<td>BI</td>
</tr>
<tr>
<td>IO7</td>
<td>Cypr</td>
<td>GPORT1_BIT3_PWM0</td>
<td>27</td>
<td>-</td>
<td>-</td>
<td>BI</td>
</tr>
<tr>
<td>IO8</td>
<td>Cypr</td>
<td>GPORT1_BIT2_PWM2</td>
<td>26</td>
<td>-</td>
<td>BI</td>
<td>-</td>
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<tr>
<td>IO9</td>
<td>Cypr</td>
<td>GPORT0_BIT3_PWM1</td>
<td>19</td>
<td>1</td>
<td>-</td>
<td>BI</td>
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<tr>
<td>IO10</td>
<td>Cypr</td>
<td>GPORT0_BIT0_PWM7</td>
<td>16</td>
<td>7</td>
<td>-</td>
<td>BI</td>
</tr>
<tr>
<td>IO11</td>
<td>Cypr</td>
<td>GPORT1_BIT1_PWM4</td>
<td>25</td>
<td>4</td>
<td>-</td>
<td>BI</td>
</tr>
<tr>
<td>IO12</td>
<td>Cypr</td>
<td>GPORT3_BIT2_PWM3</td>
<td>38</td>
<td>-</td>
<td>BI</td>
<td>-</td>
</tr>
<tr>
<td>IO13</td>
<td>Cypr</td>
<td>GPORT3_BIT3_PWM1</td>
<td>39</td>
<td>-</td>
<td>-</td>
<td>BI</td>
</tr>
<tr>
<td>IO14</td>
<td>Cypr</td>
<td>GPORT4_BIT0_PWM6</td>
<td>44</td>
<td>-</td>
<td>-</td>
<td>BI</td>
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<tr>
<td>IO15</td>
<td>Cypr</td>
<td>GPORT4_BIT1_PWM4</td>
<td>45</td>
<td>-</td>
<td>-</td>
<td>BI</td>
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<tr>
<td>IO16</td>
<td>Cypr</td>
<td>GPORT4_BIT2_PWM2</td>
<td>46</td>
<td>-</td>
<td>-</td>
<td>BI</td>
</tr>
<tr>
<td>IO17</td>
<td>Cypr</td>
<td>GPORT4_BIT3_PWM0</td>
<td>47</td>
<td>-</td>
<td>-</td>
<td>BI</td>
</tr>
<tr>
<td>IO18</td>
<td>Cypr</td>
<td>GPORT4_BIT4_PWM6</td>
<td>48</td>
<td>-</td>
<td>-</td>
<td>BI</td>
</tr>
<tr>
<td>IO19</td>
<td>Cypr</td>
<td>GPORT4_BIT5_PWM4</td>
<td>49</td>
<td>-</td>
<td>-</td>
<td>BI</td>
</tr>
</tbody>
</table>

* See Galileo I/O Function Muxing table below.

### Galileo I/O Function Muxing

<table>
<thead>
<tr>
<th>Mux Selector</th>
<th>1</th>
<th>Cypress GPIO pin</th>
<th>Linux GPIO ID</th>
<th>Dir</th>
<th>Initial Setup</th>
</tr>
</thead>
<tbody>
<tr>
<td>UART0_RXD</td>
<td>IO0</td>
<td>GPORT3_BIT4_PWM7</td>
<td>40</td>
<td>O</td>
<td>unknown</td>
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<tr>
<td>UART0_TXD</td>
<td>IO1</td>
<td>GPORT3_BIT5_PWM5</td>
<td>41</td>
<td>O</td>
<td>unknown</td>
</tr>
<tr>
<td>SPI1_SS_B</td>
<td>IO10</td>
<td>GPORT3_BIT6_PWM3</td>
<td>42</td>
<td>O</td>
<td>unknown</td>
</tr>
<tr>
<td>SPI1_MOSI</td>
<td>IO11</td>
<td>GPORT3_BIT7_PWM1</td>
<td>43</td>
<td>O</td>
<td>unknown</td>
</tr>
<tr>
<td>SPI1_MISO</td>
<td>IO12</td>
<td>GPORT5_BIT2_PWM3</td>
<td>54</td>
<td>O</td>
<td>unknown</td>
</tr>
<tr>
<td>SPI1_SCK</td>
<td>IO13</td>
<td>GPORT5_BIT3_PWM1</td>
<td>55</td>
<td>O</td>
<td>unknown</td>
</tr>
<tr>
<td>AD7298:VIN0</td>
<td>IO14</td>
<td>GPORT3_BIT1_PWM5</td>
<td>37</td>
<td>O</td>
<td>0</td>
</tr>
<tr>
<td>AD7298:VIN1</td>
<td>IO15</td>
<td>GPORT3_BIT2_PWM3</td>
<td>36</td>
<td>O</td>
<td>0</td>
</tr>
<tr>
<td>AD7298:VIN2</td>
<td>IO16</td>
<td>GPORT0_BIT7_PWM1</td>
<td>23</td>
<td>O</td>
<td>0</td>
</tr>
<tr>
<td>AD7298:VIN3</td>
<td>IO17</td>
<td>GPORT0_BIT6_PWM3</td>
<td>22</td>
<td>O</td>
<td>0</td>
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<tr>
<td>AD7298:VIN4</td>
<td>IO18</td>
<td>GPORT0_BIT5_PWM5</td>
<td>21</td>
<td>O</td>
<td>0</td>
</tr>
<tr>
<td>AD7298:VIN5</td>
<td>IO19</td>
<td>GPORT0_BIT4_PWM7</td>
<td>20</td>
<td>O</td>
<td>0</td>
</tr>
<tr>
<td>IO2 via SoC GPIO&lt;6&gt;</td>
<td>IO2 via Cypress GPORT0_BIT4_PWM7</td>
<td>GPORT1_BIT7_PWM0</td>
<td>31</td>
<td>O</td>
<td>unknown</td>
</tr>
<tr>
<td>IO3 via SoC GPIO&lt;7&gt;</td>
<td>IO3 via Cypress GPORT0_BIT2_PWM3</td>
<td>GPORT1_BIT6_PWM2</td>
<td>30</td>
<td>O</td>
<td>unknown</td>
</tr>
<tr>
<td>I2C (AD7298:VIN4 or IO18) and (AD7298:VIN5 or IO19)</td>
<td>GPORT1_BIT5_PWM4</td>
<td>29</td>
<td>O</td>
<td>1</td>
<td></td>
</tr>
</tbody>
</table>

Updated 23 October 2013.