<table>
<thead>
<tr>
<th>No.</th>
<th>Description</th>
<th>Date</th>
<th>Approved By</th>
</tr>
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<tbody>
<tr>
<td>1X</td>
<td>Initial</td>
<td>May 15, 2012</td>
<td></td>
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<tr>
<td>A</td>
<td>Release</td>
<td>June 29, 2012</td>
<td>Wang Peng Michael Norman</td>
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<tr>
<td>A2</td>
<td>Rename J10 for 1x6 connector</td>
<td>September 4, 2012</td>
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<tr>
<td>A3</td>
<td>Add R27, R28 with DNP Populate J4, J5</td>
<td>September 6, 2012</td>
<td></td>
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<tr>
<td>A4</td>
<td>Populate R27, R28 and DNP J4, J5</td>
<td>September 6, 2012</td>
<td></td>
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<tr>
<td>B</td>
<td>Release</td>
<td>September 18, 2012</td>
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<tr>
<td>B1</td>
<td>Label J1 as SWD, release</td>
<td>September 20, 2012</td>
<td>Wang Peng Michael Norman</td>
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There is a bottom-layer trace shorting pin 1 to pin 2 of jumper

**ICAP Classification:** FCP: FIUO: PUBI: SCH-27626 PDF:SPF-27626

**Drawing Title:** FRDM-KL05Z

**Page Title:** MCU

**Doc:** SCH-27626 PDF:SPF-27626

**Rev:** 01

**Date:** Friday, September 21, 2012

**Sheet:** 3 of 5

**Part:** 34

**Rev:** 01

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**SMD CONNECTOR**

There is a bottom-layer trace shorting pin 1 to pin 2 of jumper.
TO LEVERAGE OPENSDA INTERFACE IN YOUR DESIGN
PLEASE PROVIDE +3.3VDC REGULATION TO P3V3_USB FROM VOLTAGE RAIL ALSO PROVIDE +3.3VDC REGULATION TO P3V3 FROM TARGET MCU VOLTAGE RAIL.

OPEN-SDA INTERFACE

TARGET MCU INTERFACE SIGNALS

OPEN-SDA INTERFACE JTAG CONNECTOR

Target reset and bootloader mode push button

SPARE 74HC125 buffer

SCH-27626 | PDF: SPF-27626 B1
Friday, September 21, 2012
4 5
of
4 5
of
4 5